

**17.** The apparatus according to claim **9**, wherein the at least one memory and the computer program code are further configured, with the at least one processor, to cause the apparatus at least to:

- signal all supported sets of capabilities to the network during initial access to the network;
- specify which one of the sets of capabilities is active; and
- when a minor change to the active capability set occurs, signal only a changed part of the active capability set.

**18.** The apparatus according to claim **9**, wherein the at least one memory and the computer program code are further configured, with the at least one processor, to cause the apparatus at least to:

- signal to the network that the device is able to support multiple capability sets.

**19.** The apparatus according to claim **9**, wherein the device comprises physically separate parts.

**20.** A computer program, embodied on a computer readable medium, wherein the computer program is configured to control a processor to perform a process, comprising:

- determining, by a device having multiple forms and associated with a network, a current form or a change in the form of the device;
- determining new parameters according to the determined current form of the device; and
- reporting the new parameters to the network.

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